Programmable Wireless Sensor Package for CprE Students

Group: May 1624 Advisor/Client: Dr. Tom Daniels Sub-Advisor: Brian Gillenwater

Meet the Team



Yidong Liu Team Leader Electrical Engineer



Xinian Bo Team Webmaster Electrical Engineer



Niklas Jorve Communication Leader Computer Engineer



Branden Sammons Key Concept Holder Software Engineer



Jonathan Krueger Key Concept Holder Software Engineer

Presentation Overview

1. Project Plan

- a. Problem Statement
- b. Scope
- c. Budget
- d. Requirements
- e. Deliverables
- f. Schedule

2. Design

- a. Final Component List
- b. PCB Diagram
- c. Testing

3. Final Steps

Plan

- a. Demo
- b. Questions

Project Plan

Problem Statement

- CprE 185 lab teaching tools are currently outdated and unreliable
 - Nintendo Wii Controller

Design

Plan

• Arduino Esplora Gamepad

Final Steps

- e
- Design a new wireless sensor "controller" that can be used in beginner-level programming lab classes (CprE 185, EE 285) for engineering students

Ouestions

Initial Scope

- Produce approximately 30 controllers
- Design comprehensive software suite
- Create in depth instruction manual for controller setup and installation

Questions

• Create comprehensive API instruction guide

Final Steps

• Create hardware specification guide

Design

Technical Challenge - Components

Challenge

Plan

• PCB contains many different types of sensors and components

Ouestions

- Each component has varying levels of documentation
- Very time consuming to add each part to the PCB design

How we overcame this challenge

Design

- Read through components' documentation
- Breadboarded out components individually

Final Steps

Refined Scope

Design

Plan

Due to unforeseen time delays from PCB component testing:

• Scope has changed into a proof-of-concept project

Final Steps

- Board prototype will be submitted
- Basic interfacing software between controller and lab computer should be completed
- Comprehensive documentation should be created for future project leaders
- Brian Gillenwater will be developing new labs for CprE 185 utilizing controller

Questions

Budget

Plan

- Maximum \$250/controller
 - Sensors
 - PCB
 - Case
 - Batteries
 - Intel Edison
 - Miscellaneous

Design

• Budget should include PCB production cost

Final Steps

Questions

Hardware Requirements

- Must be able to communicate wirelessly via Bluetooth
- Must survive on battery power for at least 4 hours on a single charge

Questions

- Must be able to turn off controller
- Must include these sensors/components:

Final Steps

• Accelerometer

Design

- \circ Joystick
- Buttons
- Audio input/output

Software Requirements

- Sensor data must be easily accessible
- Bluetooth communication must be simplified on both sides
- Programmers must not experience interference from bluetooth commands

Questions

• Controller must be reprogrammable

Design

Plan

• Sensor sample rate must be configurable

Final Steps

Deliverables

• Wireless Controller-like prototype with multiple sensors that can communicate via Bluetooth

• Software suite alpha that can load programs into controller memory, control communication, and display sensor data

• Comprehensive Documentation for software and hardware

Questions

Final Steps

Design

Current Schedule

Design

| Week | Hardware | Software | Other |
|----------------|-----------------------------|-----------------------------------|---|
| 10 | Receive PCB from production | Modification of Prototypes | Instructor Presentation |
| 11 | PCB testing | Test Software with PCB | Documentation |
| 12 | PCB testing | Linking software via Bluetooth | Documentation |
| 13 | PCB documentation | Documentation | Poster/Presentation/Demo Development |
| 14 | PCB documentation | Finalize Demo Software | Practice Presentation/Demo |
| 15 (Dead Week) | Presentation | Presentation | Presentation |

Questions

Technical Challenge - SPI Bus

Challenge

- SPI communication new to team members
- Edison sends messages in Little Endian form
- Poor documentation covering system libraries

Final Steps

Ouestions

How we overcame this challenge

- Accounted for Little Endian form
- Lab testing with breadboards

Design

Design Specifications

Intel Edison Specifications

- Dual-core CPU at 400 MHz
- Wifi and Bluetooth 4.0 enabled
- 1 GB RAM
- 4 GB Flash memory on-board
- 20 digital I/O pins including 4 as PWM pins

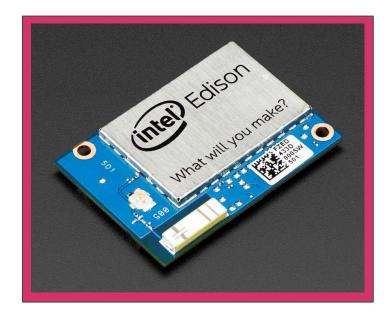
Final Steps

Questions

- 1 I²S pin (audio)
- Low power consumption

Design

• ~\$50 price tag



Final Component List

- Intel Edison
- 9 Degrees of Freedom (Accelerometer, Gyro)

Final Steps

Questions

- Joystick
- Buttons
- Status LEDs
- RGB LED
- ADC/DAC

Plan

- Voltage Regulator
- Custom Acrylic Case

Design

- Audio Codec
- Charge Circuit
- Battery Holder
- Logic Leveler
- USB-B Port
- Header Pins
- On/Off Switch
- Mic-In/Line-In/Headphone Out

Budget Breakdown

| Part Number | Description | Price |
|-----------------|------------------------------------|-------|
| H11630CT-ND | 70 pin connection for Intel Edison | 1.37 |
| MPU-9250 | 9 degree of freddom sensor | 10.64 |
| AD5592RBRUZ | ADC/DAC | 7.51 |
| SGTL5000XNAA3R2 | Audio Codec | 2.34 |
| txb0108 | Logic Leveller | 1.88 |
| 450-1650-ND | Button | 0.11 |
| 18650 | Batteries | 8.38 |
| BK-18650-PC4 | Battery Holder | 4.48 |
| ED2983-ND | USB-B female port | 0.58 |
| SC1488-1-ND | Audio Jack | 0.69 |
| MCP73831T | Battery Charge Circuit | 0.56 |
| Parts Total | 48.74 | 4 |

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Design

Questions

Budget Breakdown Cont.

Final Steps

Questions

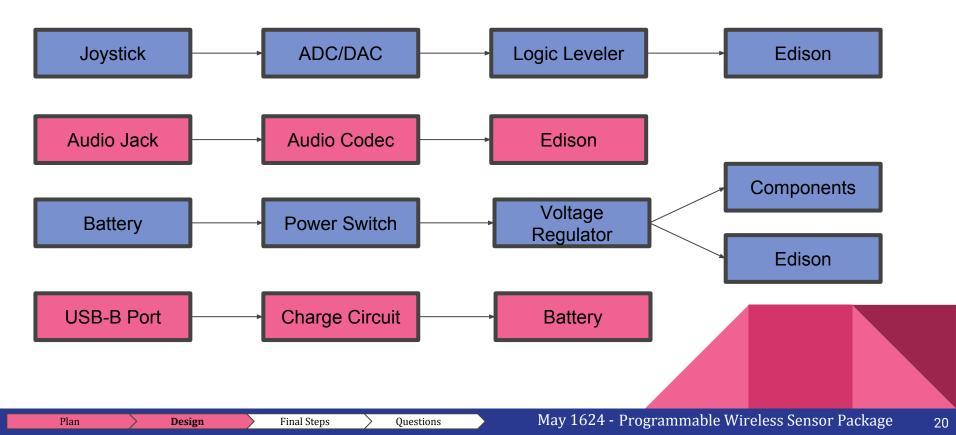
- PCB Components: ~\$49
- PCB Production: ~\$50
- Controller Case: ~\$12
- Intel Edison: ~\$50
- Miscellaneous: ~\$10

Design

• Total: ~\$171

Plan

Component Interaction Diagram



Technical Challenge - Edison I/O Voltage

Challenge

Plan

- Edison utilizes 1.8V logic
- Other chips demand 3.3V logic

How we overcame this challenge

Design

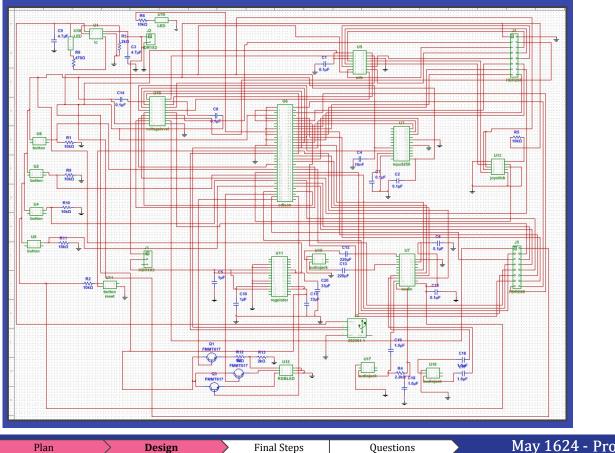
- Read component documentation
- Testing with oscilloscope to measure voltages

Final Steps

• Logic leveler used to modify logic to proper voltages

Ouestions

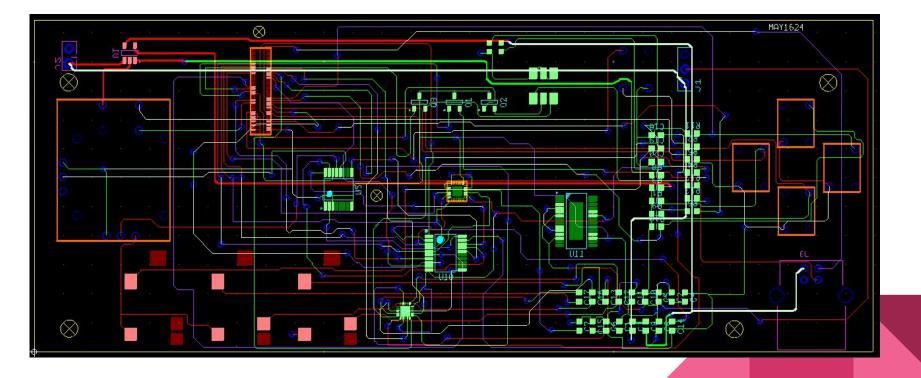
Pin Connections



PCB Layout

Plan

Design



Questions

Final Steps

Technical Challenge - Case

Challenge

Plan

• Commercially available cases are expensive

Final Steps

Ouestions

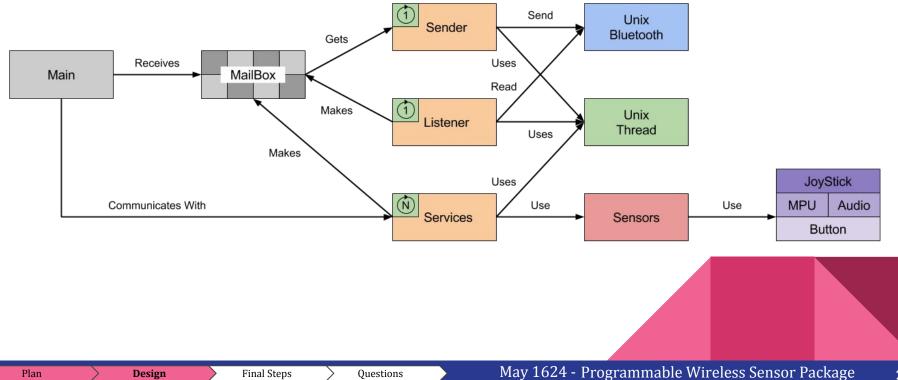
• Case needs to have easy access to batteries and PCB

How we overcame this challenge

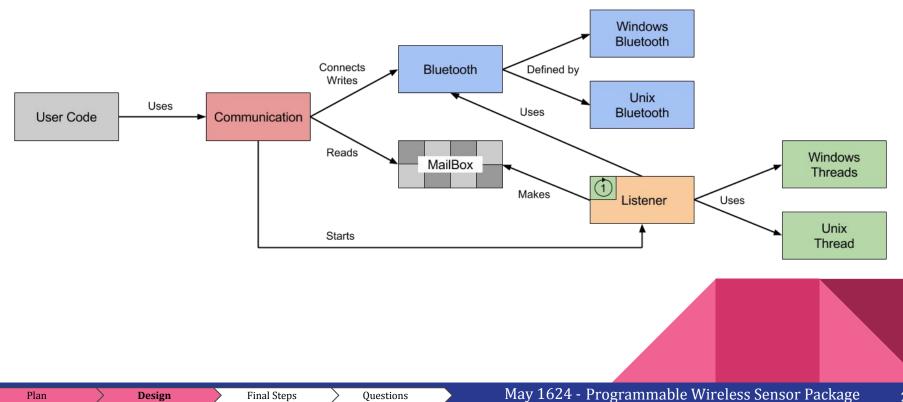
- Develop custom acrylic case
 - Cheap
 - Configurable

Design

Software Overview - Controller



Software Overview - Lab Computer



Testing

Testing Plans

- Hardware Testing
- System integration testing
- White-Box Testing
- Performance Testing
- User Experience Testing

Design

Final Steps

Questions

Final Steps

Final Steps

- PCB Modifications
- Project Plan
- Design Document
- Testing
- Documentation
- Poster/Presentation

Design

Final Steps

Questions

• Demo

Plan

Senior Design Reflection

- Cross discipline communication
- Reading technical documentation
- Mentoring from advisor
- Balancing timeline versus scope
- Managing roadblocks
- Dealing with unexpected issues

Final Steps

Questions

• Learned new skills

Design

Plan

Demo/Review/Questions

Plan

- Re-programmable Controller-like device utilizing Intel Edison
- Controller used for CprE 185 labs (possibly EE 285 labs)
- Dump data from sensors over Bluetooth to lab computer
- User-friendly interface to monitor sensors from lab computer

Questions

• Beginner level API calls to utilize sensor information

Final Steps

Design